

PALM INTRANET

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Inventor Name Search Result Office of Public Affairs

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Last Name = PARTHASARATHY

First Name = VASUDEVAN

| Application# | Patent# | PG Pub# | Status | Date Filed | Title | Examiner Name | Inventor Name |
|--------------------------|-------------------------|-------------|--------|------------|--|------------------|--------------------------|
| 11124598 | Not Issued | 20060251195 | 071 | 05/05/2005 | STATE BASED ALGORITHM TO MINIMIZE MEAN SQUARED ERROR | PERILLA,JASON | PARTHASARATHY, VASUDEVAN |
| 12071138 | Not Issued | 20080212665 | 030 | 02/15/2008 | SYSTEM FOR MONITORING THE QUALITY OF A COMMUNICATIONS CHANNEL WITH MIRROR RECEIVERS | LUGO,DAVID | PARTHASARATHY, VASUDEVAN |
| 60737292 | Not Issued | | 159 | 11/15/2005 | RECEIVE EQUALIZER WITH ADAPTIVE LOOPS | | PARTHASARATHY, VASUDEVAN |
| 11292610 | Not Issued | 20070110199 | 030 | 12/02/2005 | RECEIVE EQUALIZER WITH ADAPTIVE LOOPS | BURD,KEVIN | PARTHASARATHY, VASUDEVAN |
| 11837278 | Not Issued | 20080049847 | 030 | 08/10/2007 | ELECTRONIC DISPERSION COMPENSATION UTILIZING INTERLEAVED ARCHITECTURE AND CHANNEL IDENTIFICATION FOR ASSISTING TIMING RECOVERY | GHAYOUR,MOHAMMAD | PARTHASARATHY, VASUDEVAN |
| 11837301 | Not Issued | 20080187082 | 030 | 08/10/2007 | ELECTRONIC DISPERSION COMPENSATION UTILIZING INTERLEAVED ARCHITECTURE AND CHANNEL IDENTIFICATION FOR ASSISTING TIMING RECOVERY | GHAYOUR,MOHAMMAD | PARTHASARATHY, VASUDEVAN |
| 11845779 | Not Issued | 20080069199 | 030 | 08/27/2007 | NON-LINEAR DECISION FEEDBACK EQUALIZER | GHAYOUR,MOHAMMAD | PARTHASARATHY, VASUDEVAN |
| 11845781 | Not Issued | 20080049825 | 030 | 08/27/2007 | EQUALIZER WITH REORDER | GHAYOUR,MOHAMMAD | PARTHASARATHY, VASUDEVAN |
| 11977694 | Not Issued | 0 | 025 | 10/25/2007 | METHOD AND SYSTEM FOR TESTING DEVICES USING LOOP-BACK PSEUDO RANDOM DATAT | | PARTHASARATHY, VASUDEVAN |
| 10694945 | 7430240 | 20050094734 | 150 | 10/29/2003 | APPARATUS AND METHOD FOR AUTOMATIC POLARITY SWAP IN A COMMUNICATIONS SYSTEM | TRAN,KHAI | PARTHASARATHY, VASUDEVAN |
| 60540295 | Not Issued | | 159 | 01/30/2004 | TRANSCEIVER DEVICE WITH A TRANSMIT CLOCK SIGNAL PHASE THAT IS PHASE-LOCKED WITH A RECEIVE CLOCK SIGNAL PHASE | | PARTHASARATHY, VASUDEVAN |
| 10813235 | Not Issued | 20050169417 | 071 | 03/31/2004 | SYSTEM AND METHOD OF PHASE-LOCKING A TRANSMIT CLOCK SIGNAL PHASE WITH A RECEIVE CLOCK SIGNAL PHASE | BOCURE,TESFALDET | PARTHASARATHY, VASUDEVAN |

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|-----------------|----------------|-------------|-----|------------|---|-------------------|-----------------------------|
| <u>10813363</u> | Not Issued | 20050169355 | 041 | 03/31/2004 | TRANSCIEVER DEVICE WITH A TRANSMIT CLOCK SIGNAL PHASE THAT IS PHASE- LOCKED WITH A RECEIVER CLOCK SIGNAL PHASE | BOCURE, TESFALDET | PARTHASARATHY, VASUDEVAN |
| <u>10767729</u> | <u>7339986</u> | 20050188284 | 150 | 01/30/2004 | METHOD OF MONITORING THE QUALITY OF A COMMUNICATIONS CHANNEL | LUGO, DAVID | PARTHASARATHY, VASUDEVAN |
| <u>10767748</u> | <u>7333537</u> | 20050171994 | 150 | 01/30/2004 | SYSTEM FOR MONITORING THE QUALITY OF A COMMUNICATIONS CHANNEL WITH MIRROR RECEIVERS | LUGO, DAVID | PARTHASARATHY, VASUDEVAN |
| <u>60840123</u> | Not Issued | | 159 | 08/25/2006 | DIGITAL ELECTRONIC DISPERSION COMPENSATION FOR MULTI-MODE FIBER | | PARTHASARATHY, VASUDEVAN |
| <u>12222367</u> | Not Issued | 0 | 020 | 08/07/2008 | APPARATUS AND METHOD FOR AUTOMATIC POLARITY SWAP IN A COMMUNICATIONS SYSTEM | | PARTHASARATHY, VASUDEVAN |

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